

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination 10/666,142 SIVERO ET AL.	
		Examiner	Art Unit CASSANDRA COX	2816 Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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FOREIGN PATENT DOCUMENTS

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	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	H.B. Bakoglu, "Circuit, Interconnections, and Packaging for VLSI", Addison-Wesley Publishing Company, Inc., 1990, pages 198-219
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.